

Patent

Docket No.: Intel 2207/1012802

Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Ronald Smith

SERIAL NO. OF
PARENT APPLICATION : 09/820,703

FILING DATE OF
PARENT APPLICATION : March 30, 2001

FOR : METHOD AND APPARATUS
FOR IMPROVING RELIABILITY
IN MICROPROCESSORS

ART UNIT OF
PARENT APPLICATION : 2113

EXAMINER OF
PARENT APPLICATION : Scott Baderman

M/S: PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Please enter the following amendments in this continuation application prior to
Examination of this case.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on
page 3 of this paper.

Remarks/Arguments begin on page 11 of this paper.